HIGH PERFORMANCE MATRIX MULTIPLICATION

ON INTEL AND FPGA PLATFORMS

**by**

**Gang Li**

**A Thesis**

**Submitted to the Faculty of**

**New Jersey Institute of Technology**

**in Partial Fulfillment of the Requirements for the Degree of**

**Master of Science in Computer Engineering**

**Helen and John C. Hartmann Department of**

**Electrical and Computer Engineering**

**May 2009**